

In the claims:

Please amend the claims as follows:

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Claim 1 (Original): A display device comprising:
a plurality of pixels, wherein a polarity of a video signal to be input into the plurality of pixels is inverted, thereby resulting in brightness of the plurality of pixels being changed.

Claim 2 (Original): A display device according to claim 1, wherein said display device is a light emitting device.

Claim 3 (Currently Amended): A display device according to claim 1, wherein said display device is incorporated in at least one selected from the group consisting of a video camera, an image reproduction apparatus, a head mount display, a portable telephone, a personal computer, and a portable information terminal.

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Claim 4 (Original): A display device comprising:
a plurality of pixels; and
a source signal line driver circuit,
wherein said source signal line driver circuit comprises a switching circuit for switching a polarity of an output signal, and
a polarity of a video signal input to said switching circuit is inverted by means of a shift signal to be input into said switching circuit and a resultant signal is then input into said plurality of pixels.

Claim 5 (Original): A display device according to claim 4, wherein:
said switching circuit comprises an inverter, a first analog switch, and a second analog switch,
said video signal input into said switching circuit is input into an input terminal of said first analog switch through said inverter,

said video signal input into said switching circuit is input into an input terminal of said second analog switch,

said shift signal is input into a first control input terminal of said first analog switch and a second control input terminal of said second analog switch,

a signal obtained by inverting a polarity of said shift signal is input into a second control input terminal of said first analog switch and a second control input terminal of said first analog switch, and

signals output from output terminals of said first analog switch and said second analog switch are output from said switching circuit.

Claim 6 (Original): A display device according to claim 4, wherein:

said switching circuit comprises an inverter, a first NAND, a second NAND, and a NOR, said first NAND is supplied with said video signal through said inverter and said shift signal,

said second NAND is supplied with said video signal and a signal obtained by inverting a polarity of said shift signal,

a signal output from said first NAND and a signal output from said second NAND are input into said NOR, and

a signal output from said NOR is output from said switching circuit.

Claim 7 (Original): A display device according to claim 4, wherein said display device is a light emitting device.

Claim 8 (Amended): A display device according to claim 4, wherein said display device is incorporated in at least one selected from the group consisting of a video camera, an image reproduction apparatus, a head mount display, a portable telephone, a personal computer, and a portable information terminal.

Claim 9 (Original): A display device comprising:

a plurality of pixels, each comprising a light emitting element; and

a source signal line driver circuit,
wherein said source signal line driver circuit comprises a shift register, one or more latches, and a switching circuit, and
a polarity of a digital video signal input from said one or more latches into said switching circuit is inverted by means of a shift signal to be input into said switching circuit and a resultant signal is then input into the plurality of pixels.

Claim 10 (Original): A display device according to claim 9, wherein:

said switching circuit comprises an inverter, a first analog switch, and a second analog switch,

said video signal input into said switching circuit is input into an input terminal of said first analog switch through said inverter,

said video signal input into said switching circuit is input into an input terminal of said second analog switch,

said shift signal is input into a first control input terminal of said first analog switch and a second control input terminal of said second analog switch,

a signal obtained by inverting a polarity of said shift signal is input into a second control input terminal of said first analog switch and a second control input terminal of said second analog switch, and

signals output from output terminals of said first analog switch and said second analog switch are output from said switching circuit.

Claim 11 (Original): A display device according to claim 9, wherein:

said switching circuit comprises an inverter, a first NAND, a second NAND, and a NOR,
said first NAND is supplied with said video signal through said inverter and said shift signal,

said second NAND is supplied with said video signal and a signal obtained by inverting a polarity of said shift signal,

a signal output from said first NAND and a signal output from said second NAND are input into said NOR, and

a signal output from said NOR is output from said switching circuit.

Claim 12 (Original): A display device according to claim 9, wherein said display device is a light emitting device.

Claim 13 (Currently Amended): A display device according to claim 9, wherein said display device is incorporated in at least one selected from the group consisting of a video camera, an image reproduction apparatus, a head mount display, a portable telephone, a personal computer, and a portable information terminal.

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Claim 14 (Original): A display device comprising:
a plurality of pixels, each comprising a light emitting element, and
a source signal line driver circuit,
wherein said source signal line driver circuit comprises a shift register, one or more latches and a switching circuit,
a polarity of a digital video signal input from said one or more latches into said switching circuit is inverted by means of a shift signal to be input into said switching circuit and a resultant signal is then input into the plurality of pixels, and
an average of a time period during which all of the light emitting elements emit light in one frame period is equal to or less than a half of the maximum value of the time period during which all of the light emitting elements emit light in one frame period.

Claim 15 (Original): A display device according to claim 14, wherein:
said switching circuit comprises an inverter, a first analog switch, and a second analog switch,
said video signal input into said switching circuit is input into an input terminal of said first analog switch through said inverter,
said video signal input into said switching circuit is input into an input terminal of said second analog switch,

said shift signal is input into a first control input terminal of said first analog switch and a second control input terminal of said second analog switch,

a signal obtained by inverting a polarity of said shift signal is input into a second control input terminal of said first analog switch and a second control input terminal of said first analog switch, and

signals output from output terminals of said first analog switch and said second analog switch are output from said switching circuit.

Claim 16 (Original): A display device according to claim 14, wherein:

said switching circuit comprises an inverter, a first NAND, a second NAND, and a NOR, said first NAND is supplied with said video signal through said inverter and said shift signal,

said second NAND is supplied with said video signal and a signal obtained by inverting a polarity of said shift signal,

a signal output from said first NAND and a signal output from said second NAND are input into said NOR, and

a signal output from said NOR is output from said switching circuit.

Claim 17 (Original): A display device according to claim 14, wherein said display device is a light emitting device.

Claim 18 (Currently Amended): A display device according to claim 14, wherein said display device is incorporated in at least one selected from the group consisting of a video camera, an image reproduction apparatus, a head mount display, a portable telephone, a personal computer, and a portable information terminal.

Claims 19-45 (Canceled)